

II. Please amend the Specification as set forth below:

1. On Page 9, lines 10-14, please amend the Specification as follows:

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The output from the secondary winding 105 is inputted to a synchronous rectifier controller 100 that includes a waveform clipper ~~105~~ 110 implemented with diode D952, resistor ~~994~~ 995 and transistor 958 and these circuit elements are employed to square up the input timing waveform from the +5 output rectifier.

2. On Page 9, lines 24-25, please amend the Specification as follows:

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A stable pulse is generated and used by a timing ramp generator ~~125~~ 120 to synchronize and reset and re-trigger the timing ramp.

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3. On Page 10, lines 8-11, please amend the Specification as follows:

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The positive current source ~~135~~ 130 applies a fixed charge current to the charge integrator 135 whenever the output of the pulse width generating comparator 125 is low and the output of the transformer winding voltage dependent switch is high.

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